



First International Workshop on *“Wide Band Gap Innovative SiC for Advanced Power”*

DATE

March 7
2019

VENUE

Ecole d'ingénieurs Polytech
Université de Tours, France
Département Électronique et Energie
Avenue Marcel Dassault, 37200 Tours

Wide Band Gap Innovative SiC for Advanced Power

First International Workshop programme

March 7, 2019 Ecole d'ingénieurs Polytech - Université de Tours, France

- 08:30 Registration
- 09:00 Opening, Jean-François Michaud, Local Workshop Chairman
- 09:10 Introduction: the WInSiC4AP Project. Leoluca Liggio and Antonio Imbruglia
Session I – MOSFET processing
Session Chair: Fabrizio Roccaforte
- 09:30 Keynote Speech: State of the art of 4H-SiC MOSFET processing, Philippe Godignon, CNM-CSIC, Spain
- 10:10 Introduction to laser annealing of ohmic contacts on 4H-SiC, Clément Berger, University of Tours, France
- 10:30 Processing and characterization of gate oxides for 4H-SiC MOSFETs, Patrick Fiorenza, CNR-IMM, Italy
- 10:50 Positive Bias Temperature Instabilities in SiC Power nMOSFETs, Giuseppe Cosentino, Università della Calabria, Italy
- 11:10 Coffee Break
Session II – Characterization, Modeling and Packaging
Session Chair: Susanna Reggiani
- 11:40 Keynote Speech: Advanced characterization of 4H-SiC high-voltage devices, Dominique Planson, Ampere Lab, France
- 12:20 Heiman TCAD model for threshold instability in advanced ST power MOSFET devices, Salvatore Cascino, STMicroelectronics, Italy
- 12:40 Absolute Double side cooling - low inductance packaging: a breakthrough for cost effective inverters, Jacques Favre, aPSI3D, France.
- 13:00 Lunch
- 14:30 ECSEL JU speech
Session III – Applications
Session Chair: Jean-François Michaud
- 14:50 Keynote Speech: SiC MOSFETs dynamic robustness: challenges and opportunities for developing high-performance multi-chip modules, Alberto Castellazzi, University of Nottingham, UK
- 15:30 High Voltage Intelligent Power Switches for embedded applications, Josep Domingo Salvany, NEXTER, France
- 15:50 Engineering requirements for avionics of unmanned aerial system, Costantino Giacomina, University of Palermo, Italy
- 16:10 SiC Based Power Plant and Avionics, Pietro Rosi, OMI, Italy
- 16:30 End of workshop/Closing remarks



Registration

Please complete the form and send to susanna.reggiani@unibo.it

Please note that the workshop will be free of charge and it includes admission, coffee break and lunch.

The registration deadline is February 28, 2019.

FAMILY NAME _____ FIRST NAME _____

COMPANY/ORGANIZATION _____

FUNCTION _____

ADDRESS _____

Zip _____ City _____ Country _____

Phone _____ Email _____

WINSIC4AP PARTNER: yes no

Organization & Committee

Chairmen

Jean-François Michaud, University of Tours, France

Susanna Reggiani, IUNET, Italy

Organizers

Jean-François Michaud, Dominique Certon, Daniel Alquier, University of Tours, France

Gaudenzio Meneghesso, Susanna Reggiani, IUNET, Italy

Fabrizio Roccaforte, Patrick Fiorenza, CNR-IMM, Italy

Antonio Imbruglia, Mario Saggio, STMicroelectronics, Italy

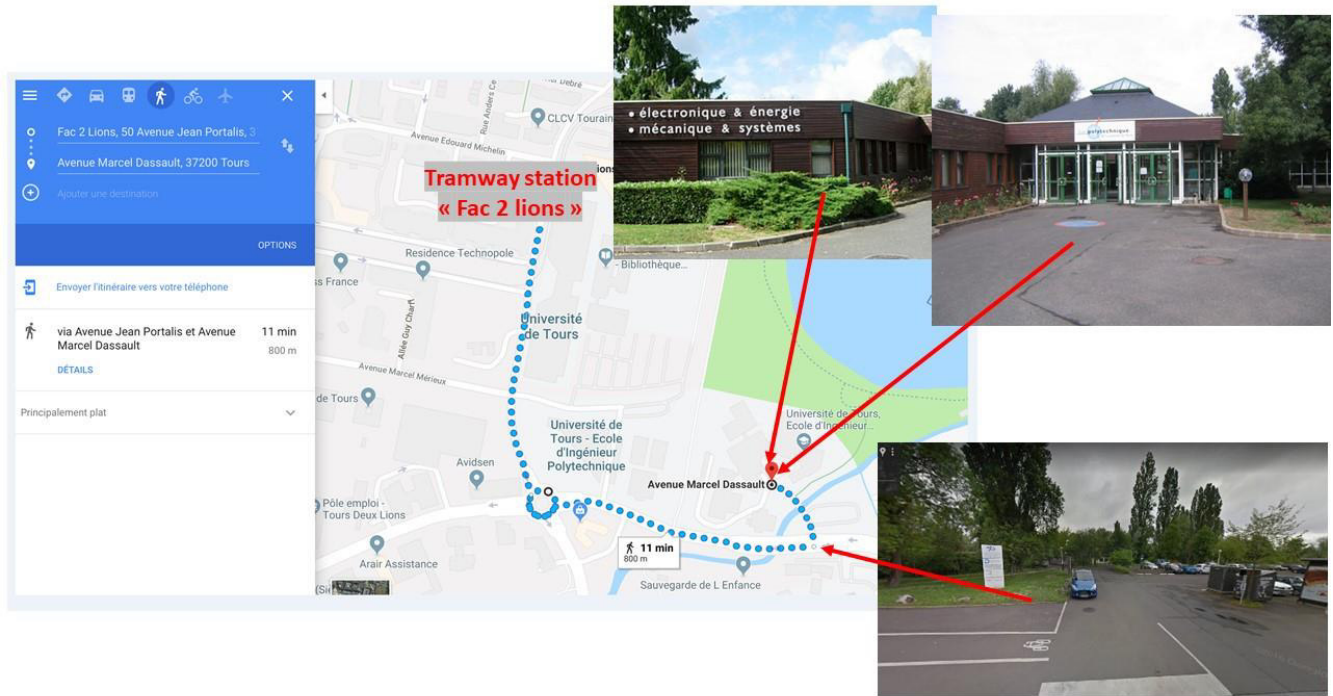
Leoluca Liggio, Salvatore Frisella, Distretto Tecnologico Sicilia, Micro e Nano Sistemi, Italy

PATRONAGE: ECSEL Joint Undertaking

Venue

Ecole d'ingénieurs Polytech - Université de Tours, France

Département Électronique et Energie, avenue Marcel Dassault, 37200 Tours



www.winsic4ap-project.org